

<b>Notice of References Cited</b>	Application/Control No. 09/975,256	Applicant(s)/Patent Under Reexamination RAMKUMAR ET AL.	
	Examiner David L. Hogans	Art Unit 2813	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0039844	04-2002	Lee, Seok-Woo	438/778
	B	US-6,294,819	09-2001	Sun, Shi-Chung	257/410
	C	US-5,464,792	11-1995	Tseng et al.	438/585
	D	US-6,436,818	08-2002	Hu et al.	438/656
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era (2000), Lattice Press, Second Edition, Volume 1, page 198.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.